

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1
Filed: March 26, 2004
Examiner: Tinh Nguyen
Customer No.: 76287

Serial No.: 10/810,035
Due Date: January 16, 2009
Group Art Unit: 2818
Confirmation No.: 8458


Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ Amendment and Response under 37 C.F.R. 1.111 (7 pgs.)

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.
Customer No.: 76287

By: 
Charles E. Steffey
Reg. No. 25,179

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 16 day of January, 2009.


Name


Signature